

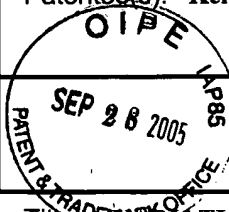
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TRANSMITTAL LETTER
(General - Patent Issued)

Docket No.
BUR920030134US1

Patentee(s): Kelkar et al.



U.S. Patent No.

6,940,324 B2

Issue Date

September 6, 2005

Title: **VARIATION OF EFFECTIVE FILTER CAPACITANCE IN PHASE LOCK LOOP CIRCUIT LOOP FILTERS**

COMMISSIONER FOR PATENTS:

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OCT 04 2005
of Correction

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CERTIFICATE OF CORRECTION

PATENT NO. : 6,940,324 B2

DATED : September 6, 2005

INVENTOR(S) : Kelkar et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 1

Line 47, delete "the first the first" and insert --the first--

Column 5

Line 18, delete " $C_{eff} = (C * G_m) / (G_m = G_d)$ " and insert $-C_{eff} = (C * G_m) / (G_m + G_d)$ --

Column 6

Line 29 and Line 32, delete "the first the first" and insert --the first--

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